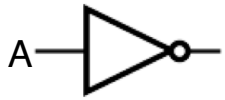


# Logic Gates

NOT



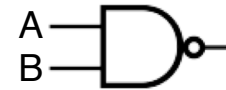
A	Output
0	1
1	0

AND



A	B	Output
0	0	0
0	1	0
1	0	0
1	1	1

NAND



A	B	Output
0	0	1
0	1	1
1	0	1
1	1	0

OR



A	B	Output
0	0	0
0	1	1
1	0	1
1	1	1

NOR



A	B	Output
0	0	1
0	1	0
1	0	0
1	1	0

XOR



A	B	Output
0	0	0
0	1	1
1	0	1
1	1	0